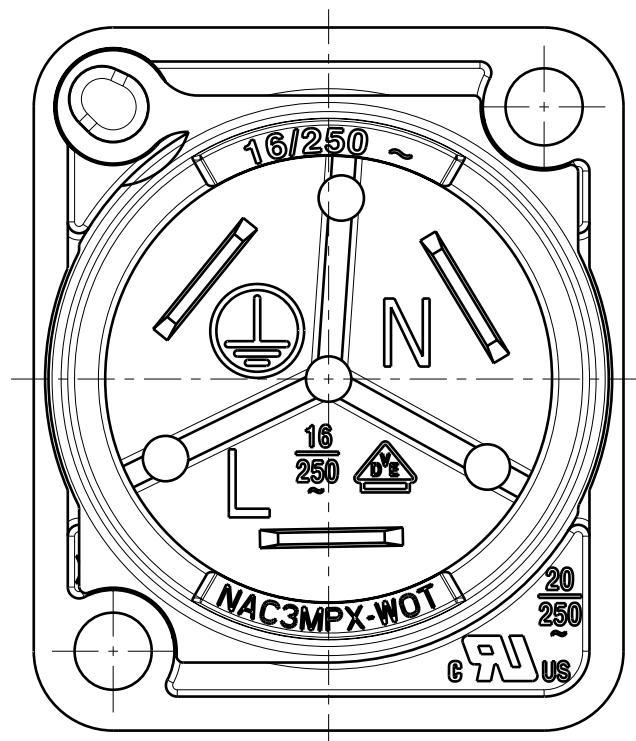
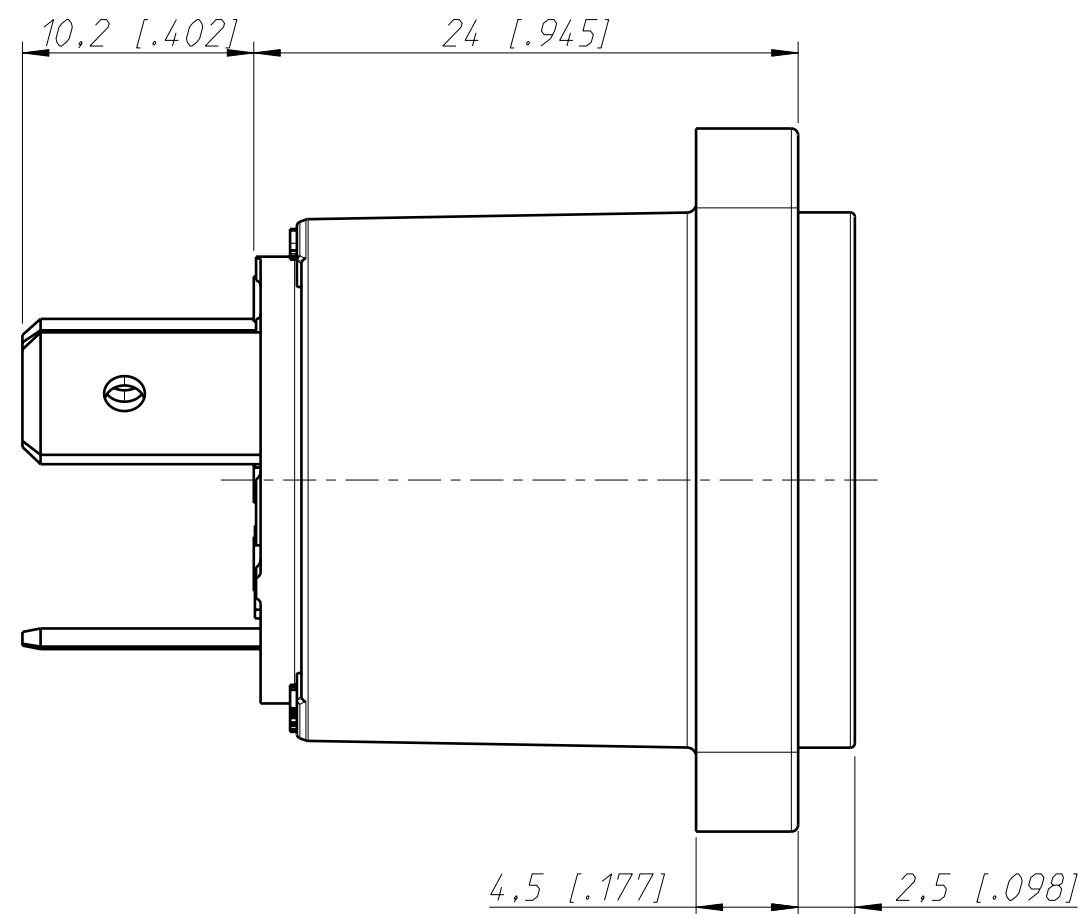
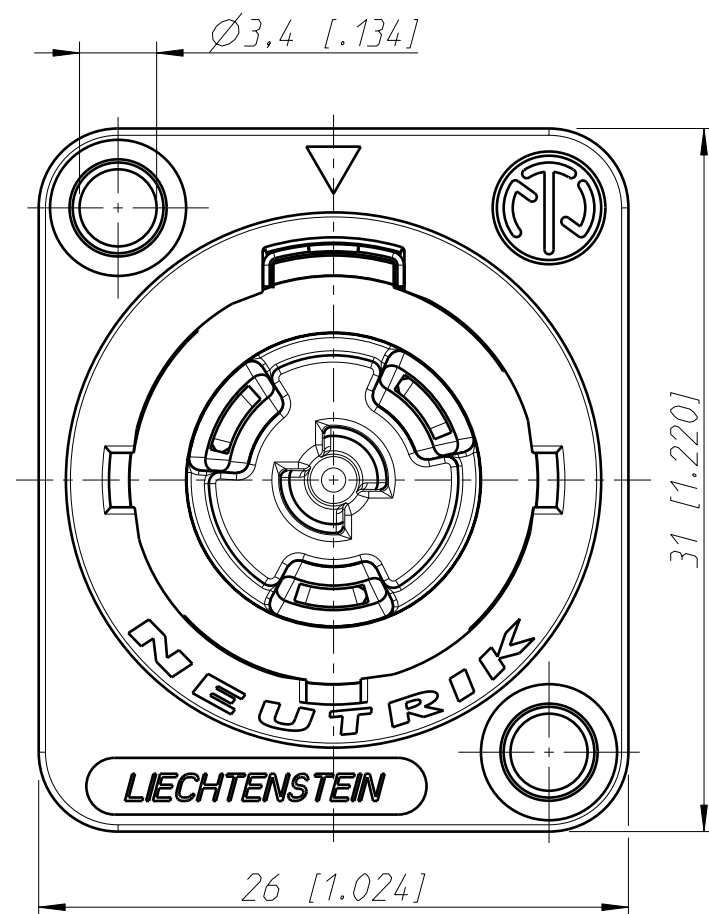
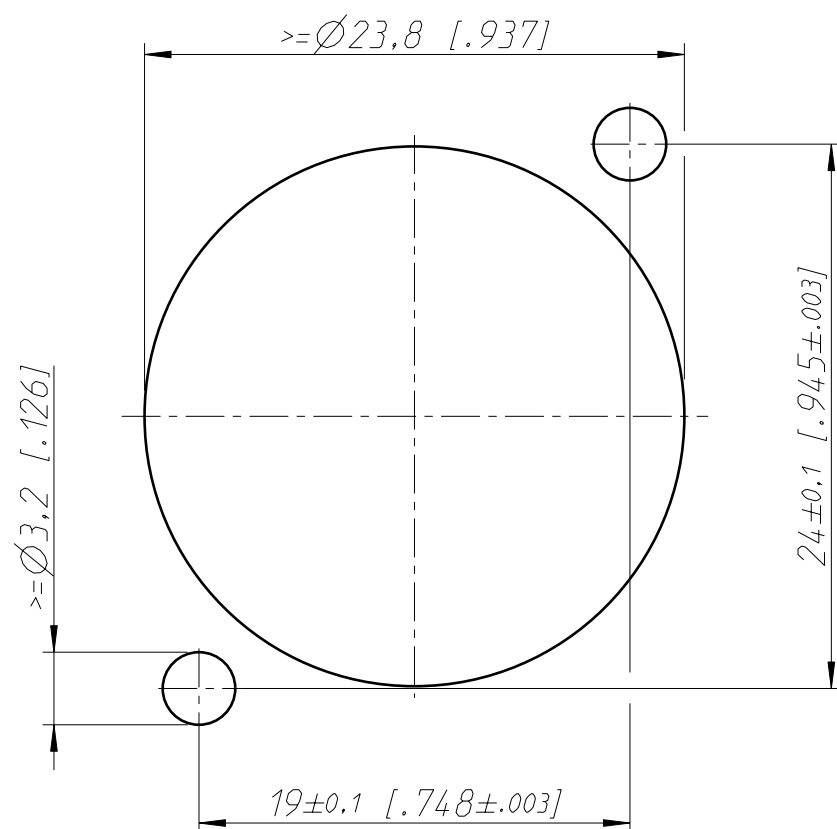
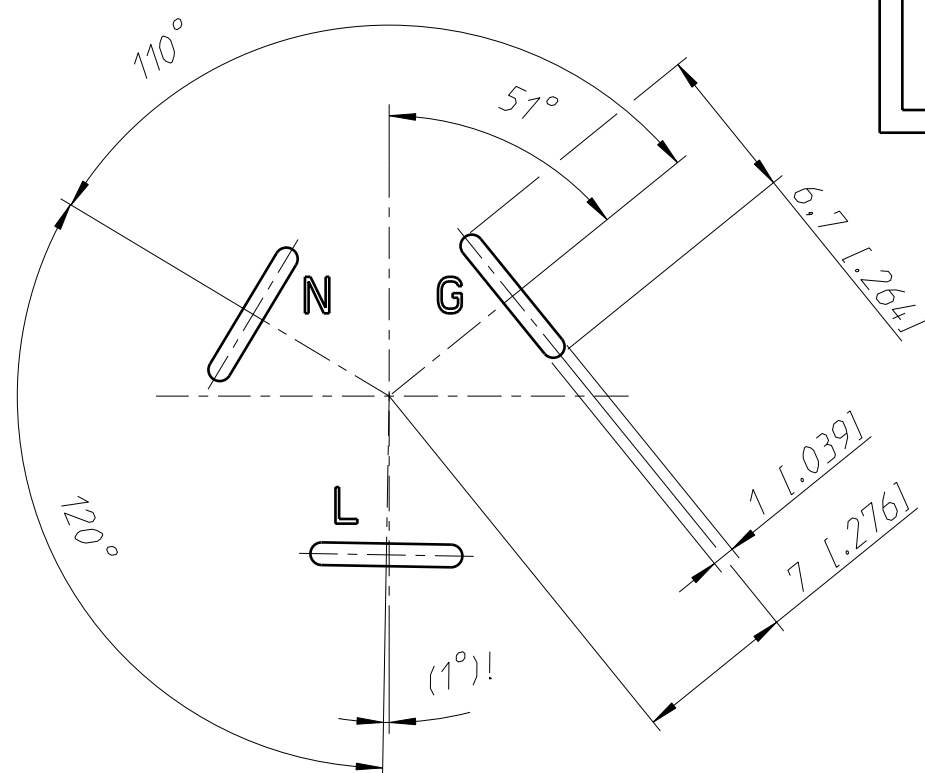




Frontplattenausschnitt Rückseite
panel cutout rear side



Leiterplatten Layout Bestückungsseite
PCB-Layout (component side)
Toleranz: ±0.05 [0.002]



APPLIANCE INLET!

General tolerances ISO 2768-m	Scale 3:1 (A3)		Date	Name
Refer to protection notice ISO 16016.		Drawn	03.05.18	Ender
Name powerCON TRUE1 TOP NAC3MPX-WOT-TOP		Modified	-	-
		Change-No. -	Sheet-No. 1 / 1	Index -
		Replacement for:		
NEUTRIK		Drawing-No. ST-NAC3MPX-WOT-TOP		